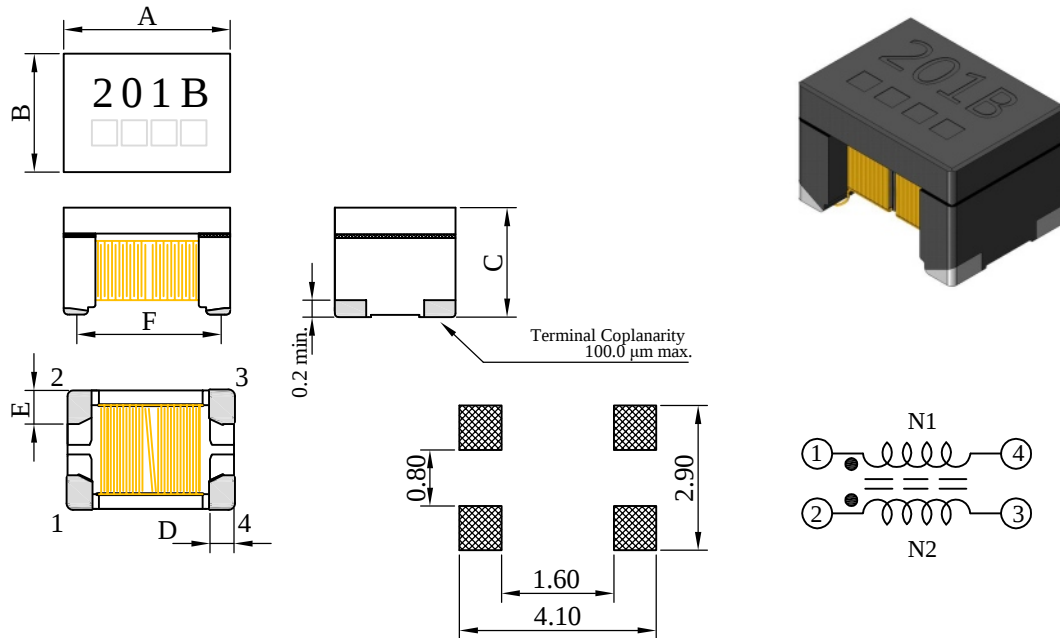


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Common Mode Filter	ABC'S DWG NO.		MSF3425□□□□BB-□□□	
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I . Configuration and dimensions :



Unit : mm

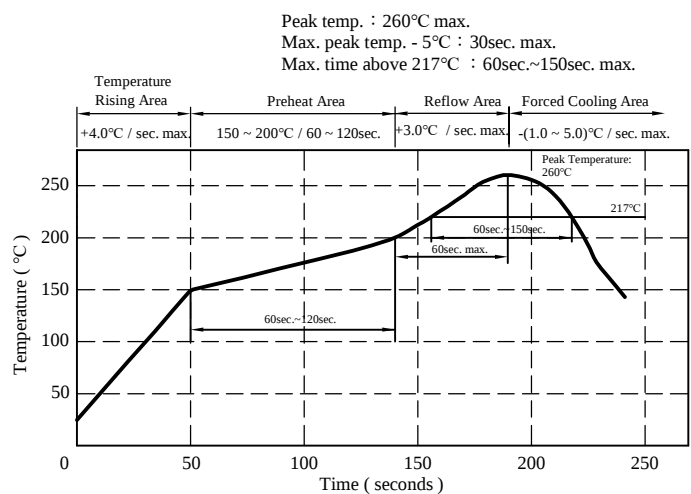
A	B	C	D	E	F
3.40 ± 0.2	2.50 ± 0.2	2.40 max.	0.60 ref.	0.65 ref.	2.80 ref.

II . Description :

- a . Ferrite drum core construction
- b . Magnetically shielded
- c . Enamelled copper wire : H class
- d . Product weight : 0.07 g (ref.)
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free
- h . AEC-Q200 Grade 1

III . General specification :

- a . Operating temp. : -40°C-----+125°C
(Temp. rise included)
- b . Resistance to solder heat : 260°C.10 sec.



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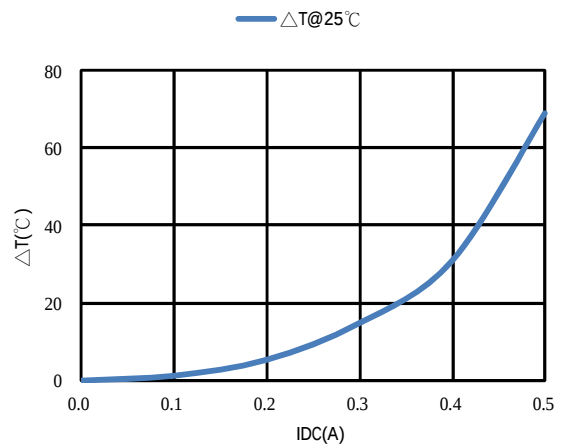
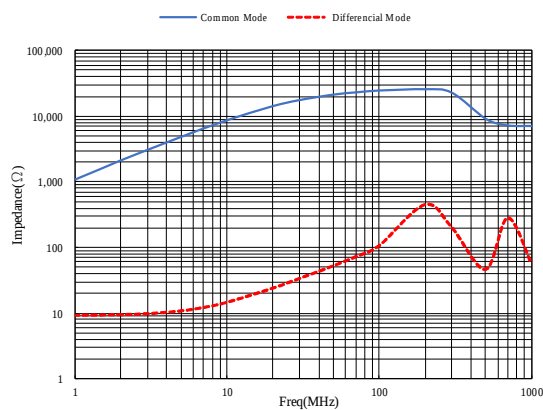
IV . Electrical characteristics :

KEY

DWG. No.	Inductance (μ H)	Lstray (μ H) typ.	RDC (Ω) max.	IDC (A) max.	Common mode impedance ($k\Omega$) (@10MHz)	
					min.	typ.
MSF3425201YBB-□□□	200.0 ^{+50%} -30%	0.50	5.50	0.25	4.00	8.50

- 1). Electrical specifications at 25°C
- 2). Inductance Test Condition. : 100kHz / 0.1V
- 3). IDC base on Temp. rise 40°C max.
- 4). Insulation resistance : 10M Ω min. @50Vdc
- 5). Rated voltage : 50Vdc

V . Curve :



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VI . S-parameter Curve (MSF3425201YBB) : (Meet Open Allience 100Base-T1)

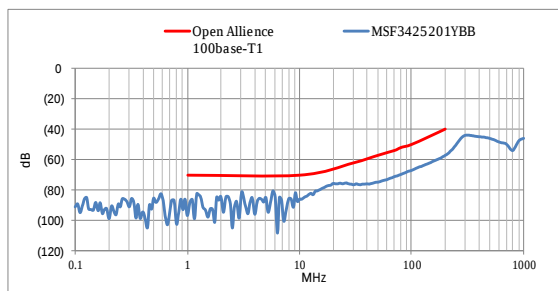
S_{ds21}	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.
	1.0	10.0	30.0	50.0	70.0	80.0	100.0	200.0
db.	-70.0	-70.0	-62.0	-57.0	-54.0	-52.0	-50.0	-40.0

S_{dd11}	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.
	1.0	10	30	50	70
db.	-27.0	-27.0	-23.0	-21.0	-19.0

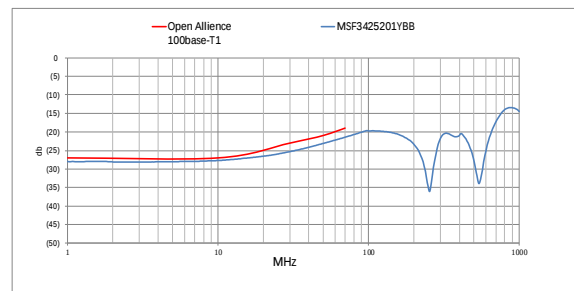
S_{dd21}	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.
	1.0	10	30	50	70
db.	-0.5	-0.5	-0.65	-0.9	-1.05

S_{cc21}	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.	Freq. (MHz) min.
	1.0	10	80	200
db.	-25.0	-45.0	-45.0	-37.0

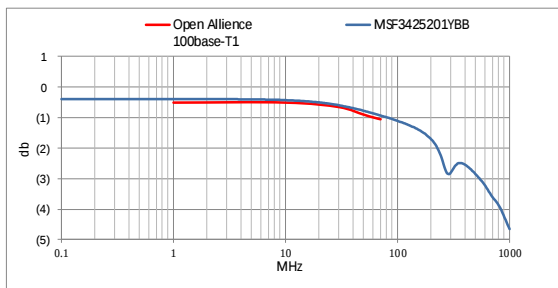
(1) S_{ds21} Curve



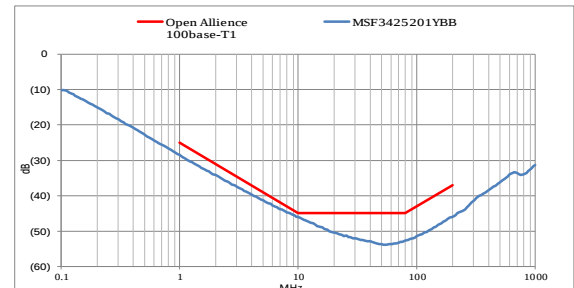
(2) S_{dd11} Curve



(3) S_{dd21} Curve



(4) S_{cc21} Curve



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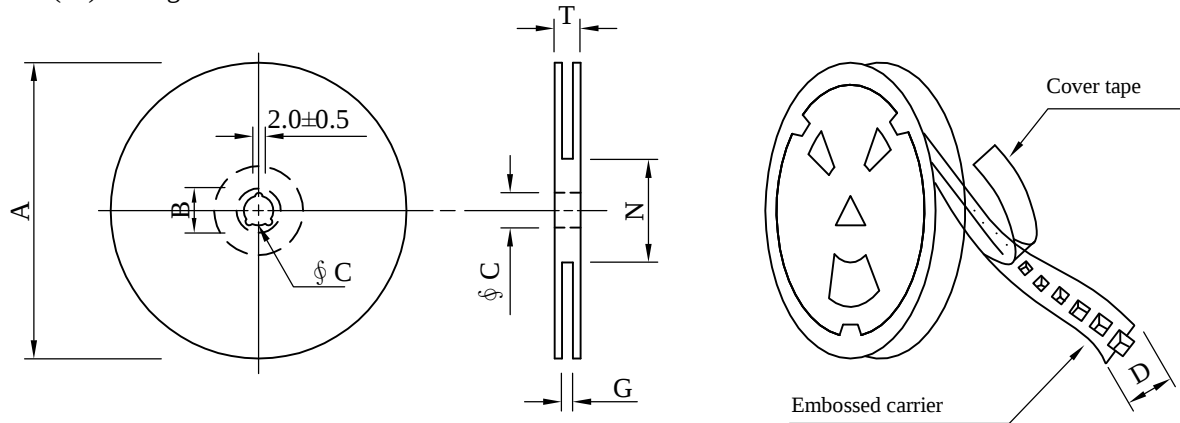
SPECIFICATION FOR APPROVAL

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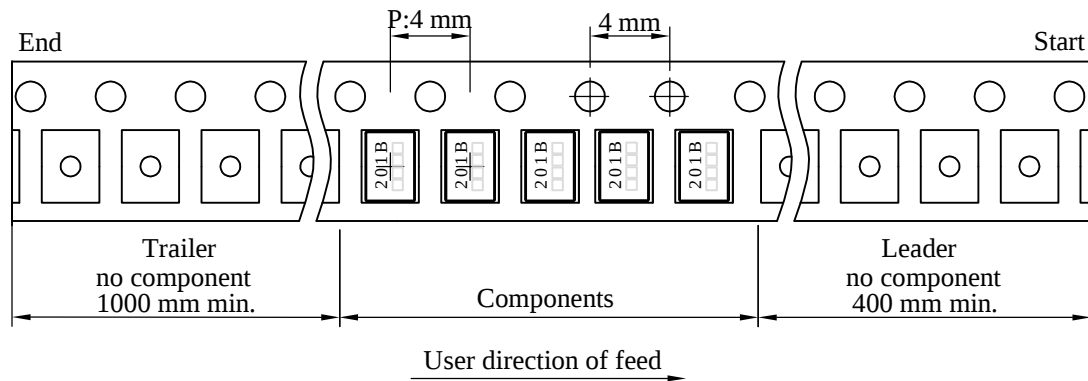
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VII . Packaging information :

(1) Configuration



※Carrier tape width : D



(2) Dimensions

Unit:mm

Style	A	B	C	D	G	N	T
07 - 12	178	21±0.8	13	12	14 ⁺⁰	50 ⁻⁰	16.5

(3) Q'TY & G.W. Pe package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (g)	Style	Q'TY (pcs)	G.W. (kg)	Size (cm)
B	1,200	150	07 - 12	48,000	7.40	41 x 39 x 22

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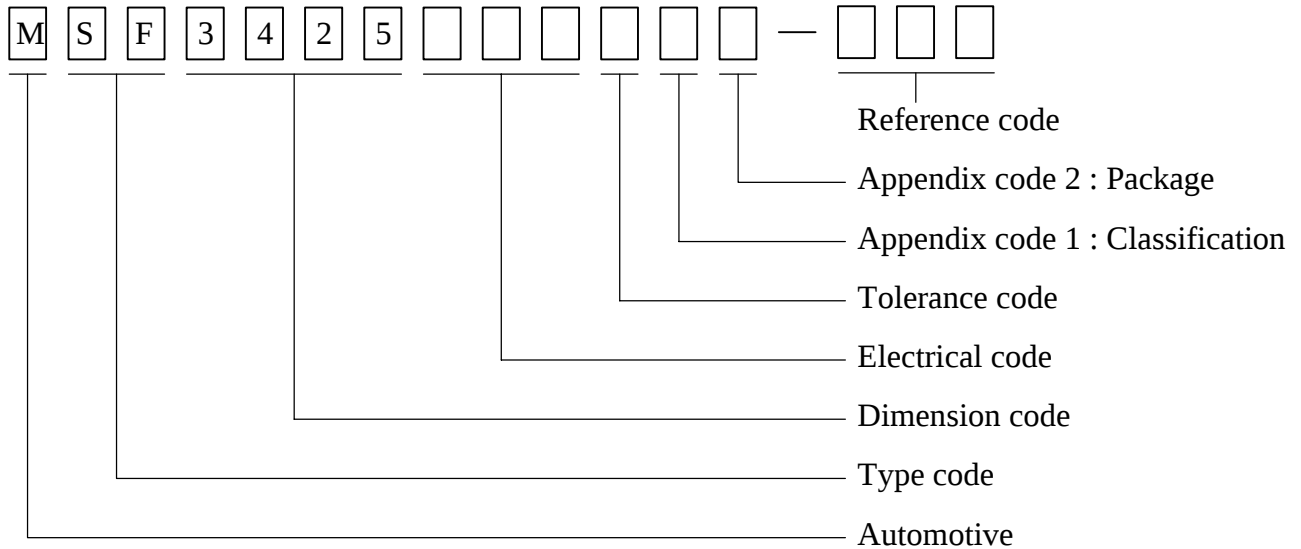
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VIII . Drawing number expression :



Appendix code 1 : Product Classification

Appendix code 2 : Package Information

Code	Inner package	Cover tape	Carrier tape	Bag	Package Q'TY	Remark
B	T /R (Reel package)	Adhesive	Non-antistatic	Non-antistatic	1,200 pcs	

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IX . Reliability test :

Item	Reference documents	AEC-Q200 Test Condition	Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125°C 2.Time:1000 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
2.Temperature Cycling	JESD22 Method JA-104	1.Temperature: -40°C ~ 125°C 2.Number of cycle:1000 cycles 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±5 °C 2.Time:1000 Hours 3.Humidity : 85±5% RH.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
4.Operational Life	MIL-PRF-27-3.26/4.7.23 & User Spec.	1.Temperature: 125°C (Temp. rise included) 2.Time:1000 hours. 3.Rated current: 0.25A	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
5.External Visual	MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22 Method JB-100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body deformation change in appearance or obliteration of marking. 2.Inductance shall not change more than ±50%.
8.Mechanical Shock	MIL-STD-202 Method 213	1.Peak acceleration100g's 2.Duration of pulse: 6ms 3.Waveform : Half-sine 4.Velocity change: 12.3ft/sec 5.Direction : ±X, ±Y, ±Z (3times / axis)	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
9.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued :10-2000-10 Hz 2.Sweep time : 20 min 3.Acceleration : 5g 4.Direction : X , Y , Z 5.Number of sweep : 12 time/axis	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
10.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020	a) Reflow : 1.Highest temperature : 260±5 °C. 2.Time(temp. ≥ 217°C): 60~150seconds. 3.IR reflow times: 3 times. b) DIP : 1. Temperature : 260±5°C. 2. Time : 10 sec. 3. Number of times : 1 time.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%. 3.RDC shall conform to specification requirements.
11.ESD	AEC-Q200-002 or ISO/DIS 10605	1.ESD Voltage : 15kV 2.Mode 1 : 150 pF / 330 Ω 3.Mode 2 : 150 pF / 2000 Ω 4.Discharge times and polarity : 3 times pos. / 3 times eng. for each condition	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
12.Solderability Test	J-STD-002	a) Reflow : 1. Baking in pre-testing:150±5°C/16Hours±30 min. 2.Peak temperature : 240±5°C. 3.Time(temp. ≥ 217°C): 60~150seconds. 4.IR reflow times: 1 time. b) DIP : 1. Baking in pre-testing: 150±5°C/16 Hours±30 min. 2. After fluxing,inductor shall be dipped in a melted solder pot at 240±5°C for 5±0.5 seconds.	Product terminals should be wetted. More than 95% soldering coverage min on terminations.
13.Electrical Characteriazation	MIL-STD-202 Method 304 & User Spec.	1.Operating temperature : -40°C~125°C 2.Room temperature : 25°C.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
14.Flammability			
15.Board Flex	AEC-Q200-005	1.Deflection speed : 1 mm/ sec 2.Amount of deflection : 2 mm 3.Span : 90 mm 4.Direction for test : Bottom of PCB 5.Holding time : 60 sec	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
16.Terminal Strength Test	AEC-Q200-006	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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X . Safety notes :

1. The risks of using the product are highly relevant to the field of application and need to be evaluated by both user and manufacturer. If the product is used for a purpose that directly affects personal safety or will cause significant impact or loss to the society , please be sure to contact us first for confirmation.
產品使用的風險與應用領域有高度相關，若您應用於直接影響人身安全系統或對社會會造成重大影響與損失之相關用途，請務必優先與我們聯繫確認。
2. To prevent our components from failing due to copper wires broken, please do not use this type of our components in your products which need (epoxy) potting or conformal coating.
為防止我們的組件因銅線斷裂而損壞，請不要在需要(環氧樹脂)灌封或保形塗層的產品中使用這種類型的組件。
3. The storage period is less than 12 months. Ensure to follow the storage conditions (Temperature: 5 to 30°C, Humidity: 10 to 60% RH or less). If the storage time is exceeded the limit, the electrodes might be deteriorate of terminal soldering.
儲存期不超過12個月，務必遵守儲存條件（溫度：5至30℃，濕度：10至60%RH以下）。如果超過了儲存時間，端子電極可能會氧化而影響焊接。
4. Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).
不要在有氣體腐蝕等條件的地方使用或存放（鹽，酸，鹼等）。
5. Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.
安裝時的焊接條件應在規格範圍內。如果超過要求，可能會發生短路，性能下降或壽命縮短。
6. When using, it should try to avoid excessive mechanical impact on the product, such as collision / drop ... and other reasons.
使用時，應盡量避免產品受到過度機械衝擊，如碰撞/掉落...等原因。
7. When embedding a printed circuit board where a chip is mounted to a set, be sure that residual stress is not given to the chip due to the overall distortion of the printed circuit board and partial distortion such as at screw tightening portions.
將已安裝新片的電路板組裝到裝置時，請注意應盡量避免電路板受到組裝變形...等，導致產品受到應力。
8. Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.
產品會因通電而自我發熱(溫度上升)，因此在熱影響設計方面，需保留適當公差。
9. Do not expose the products to magnets or magnetic fields.
請勿將產品暴露於磁鐵或是磁場中。
10. If you would like to use this products to performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property, or if you have special requirements exceeding the range or conditions set forth in the each catalog, please contact us.
如果您希望將此產品用於性能和/或質量要求更嚴格的安全性或可靠性，或其失敗、故障或麻煩可能對社會、個人或財產造成嚴重損害，或者您有特殊情況要求超出目錄中規定的範圍或條件，請與我們聯繫。

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